Rethinking Timekeeping for Modern IT Solutions

Son VoBa, Sync-n-Scale (VoBaS@sync-n-scale.com) Charles L. Ulland, Sync-n-Scale (UllandC@sync-n-scale.com) Michael A. Lombardi, NIST (Michael.Lombardi@nist.gov) Arno Lentfer, The Windows Timestamp Project (al@windowstimestamp.com)

ABSTRACT

Advances in processor performance and virtualization technologies, together with complexities of operating systems (OS) and software defined networking, have outpaced legacy network-based solutions for keeping system clocks accurate across geographic locations—in cloud-space and on-premises. Computer time accuracy with respect to Coordinated Universal Time (UTC) is now being codified and enforced by recently adopted regulatory requirements in the financial services sector for governance, risk mitigation, and demonstration of compliance.

These factors necessitate a rethinking of timekeeping for modern IT solutions. In this paper, we describe our technical approach to making UTC-accurate computer system clock a fundamental platform capability that is independent from other OS subsystems such as networking. We will also share a series of lessons learnt from building industry standard hardware interfaces to keep general-purpose computing platforms and their virtual machines UTC-accurate, and from our collaboration with industry partners Microsoft and selected server vendors to implement traceability to UTC via the National Institute of Standards and Technology (NIST) for modern IT solutions. We will conclude with our thoughts on how the full complement of Global Positioning System (GPS) positioning data in addition to accurate timekeeping would further benefit IT solutions of the future.

I. "REMEMBER THAT TIME IS MONEY" AND INACCURATE TIME CAN BE COSTLY

Benjamin Franklin was one of the founding fathers of the United States and a former Speaker of the Pennsylvania State House of Representatives. He was also an author, printer, political theorist, politician, freemason, postmaster, scientist, inventor, humorist, civic activist, statesman, and diplomat. In his 1748 essay titled *Advice to a Young Tradesman*, Franklin wrote *"Remember that time is money."* He went on to elaborate in plain terms what is now called opportunity cost in microeconomic theory.

In modern day financial markets, conducting financial services using IT resources operated with inaccurate time with respect to UTC will inevitably incur real financial costs and damaged business reputation. These costs come in the form of levied fines and publicly sanctioned findings imposed by market authorities on a per infraction basis. In the United States, the Financial Industry Regulatory Authority (FINRA) is a regulatory body charged with governing business between brokers, dealers and the investing public. A survey of its published disciplinary actions for the years 2017 and 2018 shows that FINRA imposed fines of more than \$6.5M against 31 member financial firms of all sizes for failures to keep accurate business computer time. These failures include *"report the* [in]correct time of trade", *"time stamping inaccuracies", "report order event timestamps* [not] *in milliseconds when the firm's system captured time in milliseconds", "document the* [in]correct receipt time by its financial advisors", and other infractions of similar nature. The firm Dealerweb [1] paid the smallest fine of \$12.5K for failure to report the correct trade execution time for transactions to Trade Reporting and Compliance Engine (TRACE®). The largest fines of almost \$3M were levied against Deutsche Bank Securities [2] for three episodes of numerous infractions each in this area of regulatory compliance requirements.

Financial services is one of the sixteen critical infrastructure sectors **[3]** the U.S. Department of Homeland Security has deemed vital to national security. These assets, systems, and networks, whether physical or virtual, are considered so essential that their incapacitation or destruction would have a debilitating effect on national security, economic stability, public health, and safety. These critical infrastructure components have become interdependent across vast regions, crossing jurisdictional and national boundaries and time zones. In the pursuit of "smart" operations, the need for precise position and increased dependency on accurate time have become more crucial for certain sectors such as energy, healthcare and public health, communications, and information technology. Dependency on legacy methods and tools leaves some sectors vulnerable to cyberattacks **[4]** and disruption. These attack vectors include time-shifting, *i.e.* changing time on computer systems; or preventing systems from synchronizing their clocks over the network, *i.e.* denial of service. Either causes the attacked targets to operate with incorrect time with respect to UTC.

In its February 2015 Technical Note 1867 publication **[5]** the U.S. National Institute of Standards and Technology (NIST) asserted "A new economy built on the massive growth of endpoints on the internet will require precise and verifiable timing in ways that current systems do not support." NIST went on to elaborate "Applications, computers, and communications systems have been developed with modules and layers that optimize data processing but degrade accurate timing."

The relentless advances in processor performance and virtualization technologies continue unabated. These are being fully exploited by new features and abstractions in commercial mainstream operating systems and combined with innovative software defined networking capabilities to become a rich and flexible foundation for modern IT solutions and hybrid deployments—both in cloud-space and on-premises.

Against this fast-moving tide, legacy network-based time synchronization methods and tools remain stagnant. Their utility is often only suitable for specific IT scenarios and topologies where by design the underlying components are not subjected to constant operational changes, or on-demand layers of abstraction. Attempts at modernizing them are tepid or untenable in the face of more boundary conditions borne out of how IT is delivered as a service by the industry. In many cases, the fragile network time resources that are available are unreliable or lack accuracy measurement tools.

Once a carefully curated collection of industry resources, the NTP.org roster of stratum-one time servers lost its most ardent advocate when David L. Mills of the University of Delaware retired in 2008. Elsewhere on the internet, Ask Bjørn Hansen, the developer and maintainer of the crowd-sourced and volunteer-operated NTP Pool Project, put it *"If business, organization or human life depends on having correct time or can be harmed by it being wrong, you shouldn't "just get it off the internet"."*

Keeping computers UTC-accurate remains a persistent preoccupation of IT professionals, lest bad things happen. An April 2017 tweetstorm [6] (Fig. 1) illustrated this anxiety to be well founded and casts doubt on the accuracy and reliability of commercially-backed time services offered by a worldwide recognized IT vendor.

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	So, Eme windows.com is broken. Nobody uses that for their system time, right? Who am I kiddingeveryone uses that. #Monday	Matt Keller @MattRider I hope no one is using time windows.com for ntp. It's still handing out the wrong time going on 9+ hours. Whoops.	4	I switched my synchronization to time nist gov. That did the trick for me. time windows.com wasn't working. 9:21 AM - Apr 3, 2017
♡ 2 & See Daniel Ayers's other Tweets	2 8 See Tactica/Bacon00's other Tweets	1 & See Matt Keller's other Tweets	0	

Figure 1. time.windows.com tweetstorm (April 3, 2017).

Other IT vendors adopted unconventional mechanisms to purposely alter the accuracy performance of their public facing network time services, *e.g.* leap-second smearing [7]. This is despite technical guidance to the contrary [8] from NIST.

A confluence of these emerging regulatory requirements, shared technical insights, and industry observations pointed to the need for rethinking timekeeping for modern IT solutions by:

- Introducing enterprise-grade computer hardware interfaces that exploit GPS signals as the primary and direct source for a disciplined oscillator to keep industry-standard general-purpose computer system clock UTC-accurate; and
- Collaborating with NIST and selected industry partners to devise methods and tools for definitive measurement of system clock accuracy, and for demonstration of such accuracy for regulatory compliance.

II. DESIGN APPROACHES AND FOUNDATIONAL TECHNOLOGIES

The Sync-n-Scale design approach for enabling UTC-accuracy to computer system clocks draws on the lessons learnt from the adoption of floating-point co-processor designs during the pioneering days of computing. That approach accelerated the computational speed while matching calculation precision delivered by the math software libraries that were commonly in use then. That approach also laid the foundation and charted a path for eventual hardware and software advances leading to the floating-point unit in modern day processor designs. Over the years, this same approach was taken by those in the audio video and cryptography fields.

A fundamental premise of this approach is that assured timing accuracy is an inherent computing platform capability and must persist as long as the platform is operating. An accurate system clock must not depend on auxiliary functions like networking interfaces and finicky external resources like "time servers" that are of dubious quality and trustworthiness.

This hardware-assisted approach does not impose any change requirements on user applications or deployed workloads. Nor does it require extraneous software tools added to the system. It draws on GPS time and frequency signals as a utility resource. Classic digital phase-locked loop methods are used to continually discipline the computer system clock through published kernel-mode APIs for assured persistent clock accuracy.

This approach augments the on-board time and frequency components. It transforms industry-standard servers and personal computers running mainstream commercially available operating systems into UTC-accurate general-purpose computing platforms suitable for all modern IT solutions.

The Sync-n-Scale PCIe small form-factor hardware interfaces are designed for cost-effective distribution of GPS time and frequency at data center scale. The GPS disciplined oscillator (GPSDO) interface (Fig. 2 left) connects a GPS antenna to the system. The expansion interface (Fig. 2 right) operates in tandem with the GPSDO to bring the same GPS time and frequency signals to the next system.

The Sync-n-Scale hardware interfaces allow one (Fig. 3 top and bottom left) or many computer systems (Fig. 3 right) to operate with a single GPS antenna in standard configuration. These systems and their virtual machines operate with UTC-accuracy in all environments, including those where networking connectivity is not present. The reduced attack surface would yield a higher degree of operational assurance required of modern IT solutions.





Sync-n-Scale enabled rack-mount server

Figure 2. Sync-n-Scale PCIe hardware interfaces.



in-rack servers at data center scale

Figure 3. Sync-n-Scale enablement options for server and desktop systems.

The hardware interfaces are designed to also operate seamlessly in redundant configurations for assured-timing. The standard redundant configuration can be achieved with a starting pair of industry-standard servers of same or different makes and models (Fig. 4). Additional servers can then be added to the signal distribution topology (Fig. 5) with a redundant interface configuration (Fig. 5 bottom right) or standard configuration (Fig. 5 bottom left the HPE EdgeLine EL4000 chassis houses four individual system cartridges, each with its own PCIe slot).



In these redundant configurations, there is one driver instance in each system. The driver designates one PCIe interface active and draws GPS time and frequency signals from it. When the currently active PCIe interface becomes inactive the driver automatically and seamlessly draws the signals from the other interface to continue operations.



Figure 5. Redundant and Standard Sync-n-Scale enablement.

The above active-standby redundant configuration allows modern time-aware IT solutions to operate with highavailability and assured-timing on industry-standard x64 servers from a variety of vendors. For those that demand the continuous availability offered by fault-tolerant server platforms, the hardware interfaces are also a natural fit to keep these mission critical systems UTC-accurate (Fig. 6).



Stratus ftServer x810 Series

NEC Express5800/R320 Series Figure 6. Sync-n-Scale enabled fault tolerant servers.

The fault-tolerant server architecture and lockstep hardware technology eliminate single points of failure and address the hardware, software, and serviceability issues that can lead to unplanned downtime and corruption or loss of critical data. Replicated hardware components process the same instructions at the same time. In the event of a component malfunction, processing does not miss a beat. If the system cannot automatically correct the problem, the part with the problem stops while the rest of the system continues normal operations without system downtime or data

loss. This is accomplished without the need for failover scripting, or any extra effort to make applications cluster-aware [9].

In a fault-tolerant server, a pair of PCIe interfaces are operated concurrently, each by its own device driver instance and connected to a separate GPS antenna in an active-active configuration. Measurements of 1 pulse per second (PPS) show the two interfaces diverge less than 50 ns from each other and from UTC(NIST) (Fig. 7).



Figure 7. Scope screen capture.

III. FOUNDATIONAL TECHNOLOGIES

Hardware-assisted enablement requires bringing the same GPS time and frequency signals directly and efficiently to the general-purpose computing end points, or as close to their software abstraction equivalences as possible. This allows these end points to operate applications and workloads at UTC-accuracy.

The Synchronized Crystal Oscillator (SXO) and Oven Controlled Crystal Oscillator (OCXO) are foundational technologies making this approach possible at the data center scale. Other commercial-off-the-shelf (COTS) components are designed in, including a GPS receiver module and antenna, LMR-400 coaxial of up to 100 m **[10]** in length for antenna connectivity, Cat-5 cable (with RJ45 proprietary pin-out) for expansion connectivity, FPGA for device programmability, and PCIe x1 host interface.

The SXO's frequency and phase lock to an epoch, *e.g.* UTC(USNO) via GPS signals. The interconnected SXOs operate and behave as a single crystal oscillator. All points on the physical link of the interconnected SXOs are synchronous in frequency and do not have any noticeable dynamic phase error. Static phase error (which is constant for each connected SXO node) can be corrected as needed by other elements in an SXO designed-in device. Signals on the bus are cleaned up by the recursive filtering of each SXO unit. Phase noise (and jitter) on the bus signal is as good as the best SXO in the ensemble.

Failure of any arbitrary number of individual SXO does not lead to the system failure. All remaining SXO designed-in devices stay synchronous and provide signals on the buses to be tapped off by the system. The interconnected SXOs do not require a master clock to operate, therefore there is no single point of failure.

The following diagram illustrates the Sync-n-Scale PCIe GPSDO (Fig. 8 left) and expansion (Fig. 8 right) interconnecting their respective SXOs over a multi-gigabit transceiver operating at 1.25 GHz serial bit rates to distribute GPS time and frequency signals throughout the ensemble. These physical links do not transport networking data of any type.



Figure 8. GPS signal flow through an ensemble of hardware interfaces.

The synchronicity performance of an interconnected ensemble of SXOs remains consistent across all instances regardless of their total count. By design, there is no architectural limit to the device count in an ensemble. The in-lab harness recorded this synchronicity precision performance at a constant 10 ns offset between the first and all other PCIe interfaces in the ensemble (Fig. 9).

The GPSDO PCIe interface contains an OCXO which operates at 62.5 MHz as



Figure 9. SXO precision measurement and harness.

the on-board backup frequency reference. Should the GPSDO detect GPS-denied conditions due to receiver hardware faults or external environmental factors, its FPGA firmware would take remedial steps. These include switching the 1 PPS source from the GPS receiver to the on-board OCXO, and alerting the system of this holdover transition, while maintaining the system clock accuracy in best effort indefinitely, or until GPS signals are successfully re-acquired.

The GPSDO OCXO holdover is rated for 8 µs over 8 hours (at ambient room temperature). Holdover measurements conducted at NIST-JILA laboratories demonstrated that a Windows Server 2016 system clock diverged less than 1 ms from UTC(NIST) in 21 days. Field units may vary.

The OCXO "Europack" package footprint allows drop-in of better holdover performance and future design replacement parts during manufacturing of the Sync-n-Scale GPSDO interface.

IV. OPERATING SYSTEM INTERFACE

This section focuses on Sync-n-Scale enablement of Microsoft Windows desktop and server operating systems, and Hyper-V hypervisor as platforms for Windows and non-Windows apps and workloads requiring UTC-accurate timestamps.

In addition to the GPS hardware interfaces with their design-in specialty oscillators, Sync-n-Scale creates differentiation and adds values to these software stacks through development of its device firmware and enterprise-grade Windows kernelmode device driver. The collaboration between Sync-n-Scale and Microsoft resulted in new capabilities in these software stacks for disciplining system clock, and improving accuracy of system clocks in physical systems, virtual machines, and other platform software abstractions such as containers.

The hardware interface is enabled and configured by Windows automatically or on demand through its Windows

Update computer management tool and infrastructure (Fig. 10 right). This chain of 🔝 Computer Managemen custody ensures the integrity of the driver codes, and a zero-touch hardware File Action View Help installation experience. The device driver is also available for download from the Computer Microsoft Update Catalog (Fig. 10 left) to accommodate disconnected deployments. Disk drives Display adapters DVD/CD-ROM drives Microsoft Update Catale × + B 8 A Human Interface Devices IDE ATA/ATAPI controller O ŵ □ ☆ ß rt≡ 2 A https://www.catalog.update.microsoft.com/Search.asp Keyboards Mice and other pointing devices Monitors Microsoft Update Catalog art vour search Ports (COM & LPT) Searc 💼 Print queues Processors Storage controllers Updates: 1 - 3 of 3 (page 1 of 1) 🗇 Previous | Next d Title Products Classification Last Updated Version Size vare - S2472 Time Distribution Device Windows Server 2012 R2 and later drivers Drivers (Other Hardware) 1.5 M Syst Disable n-Scale - Other hardware - S2472 Time Distribution Device Windows 8.1 and later drivers Drivers (Other Hardware) 7/15/2014 1.0.0.0 1.5 MB Downl her 🕛 Uni Uninstall -Scale - Other hardware - S2472 Time Di ution Device Windows 8 Drivers (Other Hardware) 7/15/2014 1.0.0.0 1.5 MB on. All Rights Reserved. | privacy | terms of use | help



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In a 64-bit Windows OS on "bare-metal", the kernel-mode device driver and system service operate independently of the in-box Windows Time Services (W32Time) [11]. Specifically, they do not make use of the Windows Time Provider [12] plug-in model and framework for disciplining the system clock. This clean separation of maintaining a UTC-accurate general computing platform without consuming time as a service is necessary for modern IT solutions.

The interface generates a PCIe bus hardware interrupt announcing the arrival of each GPS 1 PPS signal. In response to these hardware interrupts, the kernel-mode driver and system service "advise" the Windows OS by how much to adjust the system's time-of-day clock. These "speed-up" and "slow-down" adjustments aim to coincide the next system clock second boundary with the next arrival of the GPS 1 PPS signal, keeping these ephemeral events as close to each other as possible. The hardware and software components do not affect changes to the system clock directly.

The PCIe hardware timing behaviors and interrupts, alongside driver and OS responses can be recorded, measured and inspected individually using the Windows Performance Recorder and Analyzer [13] tools (Fig. 11).

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		Max of Duration (Fragmented) (ms)	0.007218	0.025985		
Microsoft® Windows® Performance Analyzer	Average of Duration (Fragmented) (ms)	0.007218	0.014996			
Version 10.0.10240.16384 (th1.150/09-1700)		Sum of Duration (Fragmented) (ms)	0.007218	2.459432		
Corporation. Air rights reserved.		Sum of % Duration (Fragmented)	0.00	00.0		
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Figure 11. Windows Performance Analyzer display of Sync-n-Scale kernel-mode driver performance data.

These "steering" advisory operations could complete as fast as within 7 μ s and on average within approximately 15 μ s. They may be initiated more than once per second, or not at all, depending on the system time accuracy at that moment. As a result, the system clock rapidly becomes UTC-accurate shortly after Windows completes its kernel loading phase during boot or resume. More importantly, these operations also keep the system clock persistently accurate even when it is forced to a bogus time or date by a rogue system administrator [14] or infiltrating malware.

IV.1. VIRTUAL MACHINE OPERATING SYSTEMS

Public clouds such as Microsoft Azure Cloud offer on-demand computing resources through the internet on a paid subscription basis. These cloud-based resources include Hyper-V virtual machines (VM), and other computational services such as planet-scale distributed databases, directory services, crypto key repositories, *etc.* Sync-n-Scale and server vendor Hewlett Packard Enterprise collaborated with Microsoft to bring Sync-n-Scale enabled HPE ProLiant for Microsoft Azure Stack [15] (HPE AzS) to customer on-premises data centers and private clouds. This approach to IT infrastructures allows organizations to take advantage of the rapid innovation and agility of cloud computing, and to meet regulatory requirements of their industries.

Certain timestamp accuracy requirements demanding stringent evidence-based performance [16] that could only be met by cost-effective infrastructure like Sync-n-Scale enabled HPE AzS. Collectively, the hybrid-IT [17] model (cloud-based and on-premises) and approach make it flexible for organizations to implement and deploy modern IT solutions. In addition to Microsoft Azure Cloud and its Azure Stack partners, Amazon Web Services and its Outposts partners have also started pursuing similar hybrid IT platform strategies [18].

Microsoft Hyper-V VMs enhance their performance by using the VMBus [19], which is a two-way in-memory channelbased mechanism, to communicate with their Hyper-V host. The collection of Hyper-V Integration Services [20] in the host and VMs consists of Windows system services and drivers. They are called Virtual Machine Integration Components (VMIC or "VM IC"). The comparable ones for supported Linux and FreeBSD [21] VMs are called Linux Integration Services (LIS) [22].

Most Windows VMs are set up to get important updates from Windows Update, thus these integration services are kept up to date by default in order to deliver the best performance and most recent features. Linux and FreeBSD VMs receive the latest integration components when the VM kernel is updated by the system administrator.

Many of these services are conveniences such as guest file copy, while others such as synthetic device drivers that are important to the virtual machine's functionality, and high-impact services such as time synchronization. For any given virtual machine, individual convenience services can be enabled to operate or not; while others are not intended to be serviced manually. Non-Windows and earlier releases of Windows guest operating systems might not have all available services. To work correctly, each desired integration service must be enabled on both the host and guest.

The Hyper-V Time synchronization [23] service (Fig. 12) retrieves time from the Hyper-V physical host and forwards it on to its counterpart in the VM. If this high-impact service is disabled on the Hyper-V host side, the VM system clocks will drift erratically.



Figure 12. Hyper-V Virtual Machine Manager settings.

Inside a Windows VM, the VM Time Provider (VMTP) is an active component of W32Time. The Windows VM acts as an NTP client by relying on the VMTP to synchronize its system clock with that of the Hyper-V host. The Hyper-V host acts as an NTP server at a "higher" stratum. In NTP principles, a Sync-n-Scale enabled Hyper-V host occupies stratum 1, and its VMs are all at stratum 2.

The Linux VM VMICTimeSync provider implements **hyperv** as a Precision Time Protocol (PTP) clock source **[24]** for synchronizing its system clock with that of the Hyper-V host. The Hyper-V host acts the role of a PTP grandmaster providing synchronization to multiple VM clients sharing the same VMBus network.

Regardless of the time synchronization methods and implementations, these in-memory mechanisms let the VMs take a dependency on the Hyper-V host for time synchronization with the least uncertainty factors in calculating the clock offset between the two systems. This assertion is demonstrably achievable without being heroic when the Sync-n-Scale enabled Hyper-V host time accuracy is a control variable. Conversely, if the Hyper-V host time accuracy cannot be kept persistent for any particular reason, the time accuracy of all of its VMs "suffers" accordingly.

IV.2. WORKING WITH WINDOWS CONTAINER IMAGES

Container is an emerging platform technology for modern IT solutions and is uneven in areas that might affect timeaware applications. The Microsoft base container images [25] are available with either Windows Server Core or Nano Server as the container operating system.

At this time, Nano Server base container images do not provide the full complement of time and date capabilities and tools. For modern IT solutions, time-aware Windows applications and scripts would best operate in a Windows Server Core base container image. The Windows Time service (W32Time) requires a manual start in the Microsoft base container image.

The CMD commands **time** and **date** are built-in commands. Because they are not separate executables in the Windows Container images, they need to be run in an interactive **cmd** shell, or executed, *e.g.* **docker exec –it** *<container-image>* **cmd /c time /T**. The **cmd** variables **%time%** and **%date%** are also available, *e.g.* **docker exec –it** *<container-image>* **cmd /c echo %time%**.

Given the temporary and transient nature of containers by design, architects and developers of applications that are required to demonstrate time accuracy (*i.e.* traceability) may need to explore operational and deployment methods to persist the identity of their container instance for historical references.

V. TOOLINGS FOR MONITORING AND MEASUREMENT

Modern regulatory requirements for business computer time accuracy in the financial services industry have evolved to include more stringent outcome-based performance and evidence-based governance. Business decision makers and stake holders look to their IT organizations for time accuracy performance of deployed IT solutions as an on-going governance metric to avoid regulatory enforcement consequences for not meeting these requirements.

Expertise in time metrology is not a priority for the IT community and its professionals. The scenarios they need to address might be expressed as follows:

1. At any moment, how does one know if a system clock is accurate and within a prescribed precision range of UTC?

2. In a legal proceeding, how does one prove a system clock was UTC-accurate at any given point in the past?

This section describes selected readily available tools and reproducible harnesses for IT professionals to demonstrate business computer UTC-accuracy to themselves, their users and other stake holders.

V.1. MICROSOFT WINDOWS IN-BOX AND OPEN-SOURCE TOOLS

Microsoft made engineering investments to improve time accuracy of Windows desktop and server operating systems, and Hyper-V hypervisor. Given the legacy of Windows, it is best that IT professionals filter out older on-line references, community "guidance," and commentaries pre-dating the Windows Server 2016 and Windows 8.1 releases. Microsoft developers also made available open source tools [26] that are deemed useful for measurement and characterization of system time accuracy.

w32tm.exe is the in-box preferred tool for configuring, monitoring and diagnosing the Windows Time service (W32Time). Its Time Stamp Counter (TSC) read can be used for time accuracy measurement of Sync-n-Scale enabled Hyper-V Windows VMs and Windows Containers (Fig. 13).



Figure 13. Microsoft Windows w32tm /stripchart /rdtsc command.

The TSC is a 64-bit register present on all x86/64 processors since the Intel Pentium generation. It counts the number of cycles since last reset. Invariant TSC can be found in modern processor designs and operates at a constant rate regardless of the processor operating frequency and power saving states. Both host and guest can query the common processor TSC using the instruction RDTSC. TSC reads are efficient and do not incur the overhead associated with a ring transition or access to a platform resource. When queried in short durations (*e.g.* one second apart for less than 10 minutes) the TSC values are very accurate. The command **w32tm /stripchart /rdtsc** is to be run simultaneously in both the Hyper-V host and the calibrated Windows VM guest. In each system, the tool records the system's time-of-day timestamps and corresponding TSC values. Because these values are retrieved from a common invariant TSC, they can then be used to calculate the clock offsets between the two systems directly.

For those who are inclined towards replicating the measurement harness for a mix OS environment of hypervisor host and VM guest platforms, the Microsoft tool OS time sampler **[27]** is a good starting point. It is limited to recording the local system time-of-day timestamps and corresponding invariant TSC values. The command **ostimesampler 1000 500** takes 500 samples, each 1 second apart. Additional capabilities can be added to the original source codes as appropriate.

These command line tools are best for a harness that allows them to be run in both hypervisor host and its VM guest platforms. Doing so minimizes or altogether eliminates the contributing uncertainty factors that would skew the clock offset calculation results. Hence, these tools are not practical for evaluating system time accuracy of public cloud VMs such as those in Azure Cloud because access to the underlying Hyper-V hypervisor is not possible for the general population.

Instead, comparable on-premises alternative such as the HPE AzS would be more suitable for these tools to ascertain VM time accuracy performance. In-house IT development resources, or qualified outside service organizations such as HPE Pointnext can also be called upon to create sophisticate harness using these tools for complex enterprise needs.

These command line tools generate numeric data that can be consumed by other tools. This style of analytic workflow is suboptimal for real-time and long-haul visualization of a system time accuracy performance and trend. Furthermore, such workflow would be cumbersome at scale for multiple monitored and measured systems.

V.2. THE WINDOWS TIMESTAMP PROJECT TOOL SUITE

The Windows Timestamp Project G Suite **[28]** is an independent and free tool suite. It is a ready-made expert alternative to the Microsoft in-box command line tools. The G Suite GUI includes visualizing stability of the local system clock over time (Fig. 14 top left), the monitoring of up to 64 NTP time sources (Fig. 14 top right), visualizing their clock synchronicity with



Figure 14. The Windows Timestamp Project G Suite GUI display tabs.

respect to the local system time-of-day (Fig. 14 bottom left), and visualizing the effects of contributing uncertainty factors on round-trip delays (Fig. 14 bottom right). Visualized data can also be exported for further processing by other data tools.

The G Suite GUI helps visualizing the UTC-accuracy performance of a fault-tolerant server during its failover, and of a VM during its live migration across Hyper-V hosts, and comparable migratory operations offered by other hypervisors. The annotated chart (Fig. 15) was recorded inside a Windows Server 2016 VM being live migrated across two Sync-n-Scale enabled Hyper-V hosts. The close-up snippet of recorded data shows two clock offset "spikes". Each coincided with the start of a VM continued operations on the landing hypervisor immediately after the completion of its live migration. These clock offset spikes lasted about 90 seconds before the VM clock UTC-accuracy is returned to normal.



Another application of this tool is for understanding the underlying behaviors of the hypervisors. The following measurements were conducted inside the VMs being operated by cloud vendors AWS Xen (Fig. 16 left) and Azure Hyper-V (Fig. 16 right). The recorded clock offsets show how these hypervisors affect the VM time accuracy differently despite both



Figure 16. AWS and Azure VM time accuracy charts.

A more recent measurement was taken inside an Azure Cloud Windows Server 2016 VM which had been configured according to the recent blog Time sync for Windows VM in Azure [29]. The annotated chart (Fig. 17) shows a properly configured Azure Windows Server VM would improve its UTC-accuracy to fall within the FINRA/SEC 50 ms accuracy requirement [30].

Availability of tools and consistency of harnesses shorten time to IT operational insights. Mainstream system management tools and dashboards still need to effectively integrate UTC-accuracy of IT resources as a meaningful performance metric in the overall health monitoring



capabilities of modern IT solutions. As deployments of modern IT solutions are becoming complex across on-premises and cloud-based environments, outputs from these tools not only establish an informed basis for shared understanding of time accuracy performance, they become valuable input for machine learning and AI-based operational decisions.

Nevertheless, on their own and being operated by IT professionals these tools would not be adequate without corroborating UTC-accuracy measurements.

VI. THE ROLE OF NATIONAL LABORATORIES

At this point, we have described the solution building blocks for ensuring UTC-accuracy in modern IT solutions. Regulatory requirements make it imperative for IT professionals to demonstrate that their business computers are UTC-accurate. Effective corroboration of system time accuracy requires actual measurements using well-tested methods, tools and harnesses. An assertion of system time accuracy based on assumption, inference, or deduction should be received with a healthy dose of skepticism, regardless of its origin and no matter how well intended.

The U.S. National Institute of Standards and Technology (NIST) provides official time to the United States for commerce. Elsewhere, national metrology institutes (NMIs) **[31]** or designated time and frequency metrology laboratories who contribute to UTC serve as the reference timing source for their respective country. UTC(NIST) is the US coordinated universal time scale and the national standard for frequency and time interval. It comprises an ensemble of cesium and hydrogen maser atomic clocks that are maintained at the NIST laboratories in Colorado.

The public-private partnership with NIST has played a role in shaping our rethinking of timekeeping for modern IT solutions. Sync-n-Scale entered into a cooperative research and development agreement (CRADA) with the NIST Time and Frequency Division in April 2015 [32]. There were no monetary exchanges in this partnership. The initial objective for this collaboration was the *"Development of a Modern Framework, Associated Methods and Tools to Calibrate, Verify and Measure System Clocks of Independently Operated Servers and Personal Computers."* Since then, it has expanded to include IT centric metrology practices and modern instrumentation. This section describes the relevant collaboration output of this joint effort.

VI.1. NIST DISCIPLINED CLOCK (NISTDC)

Intuitively, and in an ideal world, it would make sense to place an in-production computer physically next to a national time scale, and to utilize the full complement of NMI tools, harnesses, and subject matter experts to definitively measure its system clock divergence from UTC. Alas, it is quickly obvious that approach would not be at all practical and scalable in the IT world.

It was from that learning, that an alternative was sought. The NIST Disciplined Clock (NISTDC) is an available offering from NIST as an optional add-on to its Time Measurement and Analysis Service (TMAS).

The NISTDC is an active monitoring and measurement instrument for modern IT solutions (Fig. 18). Each NISTDC is a replica of the US time scale for time interval (second) and frequency (hertz) metrology purposes. It is highly calibrated and contains no self-serviceable hardware and software parts.



Figure 18. Sync-n-Scale operated NISTDC unit.

The NISTDC operates as an internet connected device with its own GPS antenna and calibrated coaxial cable. It uses both the internet, and common-view and all-in-view [33] observations of GPS satellites, and is directly referenced to the UTC (NIST) time scale in Boulder, Colorado. These capabilities allow the NISTDC to be deployed and functional worldwide.

The NISTDC is built on a BitLocker-protected **[34]** 64-bit Windows 10 platform. The NIST instrumentation and calibration software disciplines an in-chassis rubidium clock to agree with UTC(NIST). This makes it possible to definitively establish UTC-traceability of the monitored and measured systems to the International System (SI) Units Time **[35]** directly through NIST.

The NISTDC frequency uncertainty is rated at less than 1×10^{-14} after one day of averaging with respect to UTC(NIST), and time uncertainty is 12 to 15 ns with respect to UTC(NIST). These NISTDC performance metrics and its actual geolocation are continuously recorded in the NIST data repository and can be viewed using a standard web browser.

The NISTDC can be deployed inside the IT perimeter of an organization, and within intranet proximity of its monitored and measured in-production computer systems, while operating outside of the management domains of those systems. It does not analyze or graph time accuracy data. It collects and stores the measurement data locally, before forwarding them over the internet to the NIST repository for archival. UTC(NIST)-accuracy analysis of the monitored and measured systems can then be securely viewed in tabular and graphical form using a standard web browser.

A Sync-n-Scale operated NISTDC had been calibrated at NIST in Boulder, Colorado prior to shipment to its current location in Burlington, Wisconsin. The configured NIST software tools continue this calibration on-site and the accuracy of the UTC(NIST) replica can be examined in real time. An RMS 0.03 ns divergence from UTC(NIST) is nominal in this 200-day measurement ending on January 9, 2019 (Fig. 19).



NISTDC versus UTC(NIST) via Common-View GPS

Figure 19. NISTDC v. UTC(NIST) 200-day accuracy chart.

This NISTDC employs the "common-view" technique to keep its replica of UTC(NIST) extremely accurate. Common-view is a simple but effective method for comparing clocks. The time difference between a NISTDC and the UTC(NIST) can be measured by simultaneously comparing both clocks to a signal that is in common view of both sites. The difference between the two comparisons is the time difference between the two clocks. The common-view signal is simply a vehicle used to transfer time from one site to the other. The accuracy of this common-view signal is irrelevant because it does not influence the final measurement result.



Figure 20. GPS common-view technique.

The GPS satellite S transmits signals that are received at NIST in Colorado and at the Sync-n-Scale NISTDC in Wisconsin (Fig. 20). Both sites compare the received signals to their local clock. The measurement at NIST compares the GPS signals received over the path d_{S-NIST} to the US time scale, *Clock NIST* – *S*. The NISTDC compares GPS signals over the path d_{S-NIST} to the US time scale, *Clock NIST* – *S*. The NISTDC compares GPS signals over the path $d_{S-NISTDC}$ – *S*. Delays that are common to both paths d_{S-NIST} and $d_{S-NISTDC}$ cancel even if they are not known, but uncorrected delay differences between the two paths add uncertainty to the measurement result. Thus, the basic equation for a common-view GPS measurement is:

$(Clock NIST - S) - (Clock NISTDC - S) = (Clock NIST - Clock NISTDC) + (e_{S-NIST} - e_{S-NISTDC})$

The components that make up the ($e_{s-NIST} - e_{s-NISTDC}$) error term include delay differences between the two sites caused by ionospheric and tropospheric delays, multipath signal reflections, environmental conditions, or errors in the GPS antenna coordinates. These factors can be measured or estimated and applied as a correction to the measurement, or they can be accounted for in the uncertainty analysis. It is also necessary to calibrate the GPS receivers used at both sites and account for the delays in the receiver, antenna, and antenna cable. These necessitate the NISTDC being calibrated at NIST prior to field deployment.

UTC(NIST) in Colorado comprises of the primary NIST time scale in Boulder and the secondary NIST time scale in Fort Collins. A new calculated clock difference between the NISTDC rubidium clock and UTC(NIST) in Colorado is taken every 10 minutes. A "steering" correction is sent to the NISTDC rubidium clock. The steering correction is always a dimensionless frequency correction, and time errors are corrected through frequency adjustments [36].

The NISTDC starts steering the rubidium clock automatically as soon as it completes the Windows 10 boot. This active steering of the NISTDC rubidium clock makes it a UTC(NIST) replica.

VI.2. NTP AS A CLOCK OFFSET CALCULATION METHOD IN NISTDC

The NISTDC acts the role of an NTP client. The monitored and measured target system acts the role of an NTP server.

The basic operation of NTP protocol [37] is that a client sends a packet to a server and records the time the packet left the client in the Origin Timestamp field (T_1). The server records the arrival time of that packet as the Receive Timestamp (T_2), and assembles the response packet with the Origin Timestamp and Receive Timestamp. The server includes the departure time of the response packet as the Transmit Timestamp when it is sent back toward the client (T_3). The client records the arrival time of the response packet as Destination Timestamp (T_4). At the conclusion of this NTP transaction exchange, the server is in possession of the first three timestamps (T_1 , T_2 , and T_3), whereas the client is in possession of all four timestamps (T_1 , T_2 , T_3 and T_4).

Due to the execution complexity of their own software and networking stacks, an NTP transaction between any two equally capable NTP servers in practice is not commutative. When both systems simultaneously initiate an NTP transaction to calculate the clock offset between themselves, the results are contradicting virtually without fail. That is, while the clock offset amount might vary, both would declare identically the other's time-of-day to be ahead, or behind, compared to its own. That is an impossibility, but which one is incorrect in that particular assertion?

The NISTDC uses the current value of its UTC(NIST) replica as Origin Timestamp (i.e. T₁) in the NTP message enquiring the current time of the target system and for subsequent clock offset calculation. The NISTDC does not use its Windows 10 system time-of-day for NTP transactions.

In addition to T₁, T₂, and T₃ which are common artifacts in the NISTDC and its target system, each side also possesses the networking identity (ID) of the other which is used for the NTP packet exchange. A tuple of $(ID_{NISTDC}, ID_{TARGET}, T_1, T_2, T_3)$ encodes the directional semantic of the NTP packet exchange. It is sufficiently unique to allow for mutual corroboration of the clock divergence between the NISTDC and its target system (Fig. 21).

VI.3. NISTDC AS A UTC(NIST) FREQUENCY STANDARD

On the back of the NISTDC chassis (Fig. 22 right), there are three standard 10-MHz frequency outputs, and eight 1 PPS timing outputs. All are synchronized or syntonized to UTC(NIST). These signals can be directly tapped, or distributed farther within the facility.

The GPSDO PCIe interface is designed with its own 1 PPS out MCX port (Fig. 22 left). Measuring the signal synchronicity of this port against that of a NISTDC 1 PPS port (Fig. 7) establishes the frequency stability baseline of the hardware interface and its SXO and OCXO components being used for frequency controls components. Performance of newer parts can be characterized prior to manufacturing production.

10 MHz 1 PPS PPS 0 10 MHz 1 PPS 1 PPS

10 MHz

1 PPS

1 PPS

Figure 22. Sync-n-Scale GPSDO and NISTDC timing output ports.

A more complete UTC-accuracy performance characterization of a Windows Server system and its Hyper-V VMs in steady state would emerge from a combination of the above hardware measurements with those from system level software tools discussed earlier. These gained insights would also be valuable during GPS holdover or to understand faulttolerant server failover performance.

Applying this method, UTC-accuracy of new OS platform capabilities and software improvements can be characterized and quantified prior to production releases. This information allows time-aware workloads and applications higher up in the software stacks to operate with confidence.

VI.4. NISTDC FOR CONTINUOUS UTC-ACCURACY MONITORING AND MEASUREMENT

The NISTDC is fundamentally a time and frequency measurement instrument for IT resources. Not unlike other sophisticate devices that would produce false results when operated without an appropriate harness.

The Sync-n-Scale operated NISTDC has been configured to measure UTC-accuracy of VMs running modern Windows server and desktop OS releases. A harness was constructed to validate the VM IC Time Synchronization performance (Fig. 12). This section discusses the relevant findings from this exercise.

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Figure 21. NTP protocol exchanges.

GPS ANTENNA





Figure 23. NISTDC measurement harness for physical servers and virtual machines at Sync-n-Scale Burlington, WI site.

In this harness (Fig. 23), all four VMs (**SV-001**, **SV-002**, **LV-001** and **LV-002**) synchronize their clocks with that of their respective Hyper-V platforms (**P-A15** and **P-A21**) using VM IC Time Synchronization. The physical Windows Server **P-A21** is Sync-n-Scale enabled and operates as a W32Time NTP time server. The physical Windows Server **P-A15** synchronizes its clock with the NTP time server **P-A21**. The system OSes are as marked.

In the NTP formula for calculating clock offsets between the NTP time client and its time server, the round-trip delay is assumed to be same. Modern networking gears are sophisticate and will figure out the optimal paths for every data packet based on the current traffic conditions of the interconnected fabric. That cleverness plays against NTP and all other network-based time synchronization techniques. The optimization of flight paths for each packet is totally hidden and out of the control of the two communicating end-points. This harness is focusing on keeping the time it takes for the outgoing and incoming NTP packets to traverse evenly in both directions.

The NISTDC comes with two GbE NIC ports. One is used for internet communications with NIST in Boulder. The second one is used for networking with the on-site monitored and measured systems via an isolated GbE unmanaged switch. This connectivity scheme ensures clock offset measurement NTP packet traffics traverse consistent paths between the NISTDC and its target systems, and avoid nondeterministic asymmetry in their round-trip delays.

The NISTDC acts the role of an authoritative witness and archivist of a system UTC-accuracy. It sends UTC-accuracy measurement data every 10 minutes to a NIST archival repository on the internet. A maximum of 200 days worth of this data can be charted at one time and viewed using any web browser. Each chart shows the UTC-accuracy performance obtained by two measurement and calculation methods: "Average" (AVG) and "Minimum" (MIN).

The AVG method involves comparing a target system to UTC(NIST) replica in the NISTDC every 10 seconds, and then averaging these measurements for 10 minutes (60 measurements). UTC(NIST) replica is available with 100-ns resolution. The difference between the two 10-minute averages is the difference between the time broadcast by the target system and UTC(NIST).

The MIN method compares a target system to UTC(NIST) replica in the NISTDC every 10 seconds for a 10-minute interval (60 measurements). However, only one of the 60 measurements is saved, the one with the shortest round-trip delay. This method saves only 1.67% of the measurements, and is based on the assumption that NTP measurements with the shortest round-trip delays provide the best estimate of the true time difference. UTC(NIST) replica is available with the same 100-ns resolution. The time difference between the target system and UTC(NIST) is obtained by subtracting the two time readings obtained from the measurement with the smallest round-trip delay.

The 60-day (ending on January 8, 2019) UTC-accuracy chart shows the Sync-n-Scale enabled Windows Server 2016 VM **SV-001** (Fig. 24 left) to be nominal at 9.5 μ s diverged from UTC(NIST) on average. Another chart of the same time period shows the Sync-n-Scale enabled Windows 10 VM **SV-002** (Fig. 24 right) to be nominal at 10.0 μ s diverged from UTC(NIST) on average.



Just as it is important to have a quantified UTC-accuracy precision value, the UTC-accuracy performance graph line pattern over a meaningfully long observation period says a great deal about the underlying clock stability and adjustment mechanisms. A "flat" and "skinny" UTC-accuracy graph line indicating a time difference close to 0 on the X-axis is most desirable. Those graph lines (Fig. 24) illustrate the accuracy and stability of the measured system clock. These are visual cues that would shorten the time to insights for IT professionals.



Figure 25. NISTDC UTC-accuracy charts of Windows Server 2016 and Windows 10 VMs at Sync-n-Scale Burlington, WI site.

The next set of 60-day (also ending on January 8, 2019) UTC-accuracy charts quantify the precision of Windows Server 2016 VM **LV-001** (Fig. 25 left) to be nominal at 150.4 µs diverged from UTC(NIST) on average; and that of Windows 10 VM **LV-002** (Fig. 25 right) to be nominal at 128.6 µs diverged from UTC(NIST) on average.

The quantified differences in these charts (Figs. 24 and 25) are also visually pronounced because of the 60-day worth of measurements being examined. A shorter measurement or charting period might not have yielded similar insights.

Despite the fact that the VMs **LV-001** and **LV-002** (Fig. 25) are operating identical Windows OS bits (copies of the same Hyper-V VHD OS images), their UTC-accuracy on average were 10+ times worse than that of the other VMs **SV-001** and **SV-002** (Fig. 24). This performance difference between the two sets of VMs (Fig. 23) was during the same time period, in the same harness and under the same operating conditions, except for the mechanism used by their respective Hyper-V for disciplining its own clock.

A closer look at the Hyper-V host **P-A15** UTC-accuracy is warranted (Fig. 26) because this Windows Server 2016 system relies on an NTP time server (**P-A21**) for its clock accuracy. It is operating in a favorably constructed harness according to guidance in the blog article Accurate Time for Windows Server 2016 [38]. The outlined critical factors for accurate time were adequately met, *i.e.* solid source clock, stable client clock, and symmetrical NTP communication.

The 60-day UTC-accuracy of Hyper-V host **P-A15** was nominally at 196.3 μ s diverged from UTC(NIST) on average. Because of this time accuracy performance, all NTP clients of **P-A15** at lower strata cannot hope to perform better.



Figure 26. NISTDC UTC-accuracy chart of Windows Server 2016 physical server at Sync-n-Scale Burlington, WI site.

Nevertheless, the UTC-accuracy performance of **P-A15** and that of its VMs **LV-001** and **LV-002** during this time period was still wildly much better than had they been configured to keep the default **time.windows.com** as the NTP time source.

VI.5. NISTDC AT DATA CENTER AND PLANET SCALES

At this point, we have described the NISTDC as an instrument for monitoring and measuring UTC-accuracy of Windows physical systems and their virtual machines at server rack scale. By design, its capabilities are identical at data center and planet scales owing to the general uniformity of GPS as a global reference time source.

This property makes the NISTDC also an independent authoritative instrument for continuous UTC-accuracy monitoring of planet-scale distributed workloads and solutions across data centers to ensure their optimal performance. Sync-n-Scale enabled general-purpose computing platforms have been shown capable of operating these workloads at the highest level of UTC-accuracy precision. Being persistently UTC-accurate means natural synchronicity without developers, implementors and support IT professionals being heroic.

Blockchain consortium is an example needing UTC-accuracy for technical compliance, and monitoring for operational awareness. Distributed database is another example. These needs are same at rack, data center and planet scales.

VI.6. HIGH-PERFORMANCE PLANET-SCALE DATABASES

Time-series data are generally timestamped measurements or events that are tracked, monitored, aggregated and stored over time. Time-series data applications are proliferating mostly due to digital transformation of industries to take advantage of machine learning and other advances in data science.

A distributed database stores copies of data in multiple locations in order to efficiently receive and deliver them. These database location instances are linked to allow access and updates to these copies of data as one.

Modern IT solutions typically are geographically global in nature. Their applications and databases are geographically distributed for high availability and for maximum throughput. The computing platforms maintain clock synchronicity to preserve the integrity of time-series data being gathered by the applications. The linked data stores maintain clock synchronicity across global distances to efficiently operate as high-performance databases. All need high-speed network connectivity among these locations.

Sync-n-Scale enabled persistent UTC-accuracy across the compute and storage platforms ensures their natural synchronicity at planet scale. NISTDC corroborated UTC-accuracy measurements provide continuous operational awareness and insights for performance diagnosis.

The orchestral music score metaphor **[39]** is one way to illustrate this high-performance potential gain. The example IT solution is operated out of three data centers (Fig. 27 top) in the US, EU and Australia. Its time-series distributed database spans across these three locations.

Data written to its West Europe data store can be read by applications at other two locales in the order represented by the music notes on the staffs denoting the different sites (Fig. 27 middle). The quarter note tempo represents one level of synchronicity precision across the three sites.

Transaction order can be established quicker when there is tighter synchronicity (UTC-accuracy) precision across the platforms. These musical notes could now be "played" at a faster eighth note tempo (Fig. 27 bottom). This represents the data being available for reads outside of its write location faster across the distributed database. This performance gain benefits modern IT solutions in all industry sectors, not just financial services.

VII. LOCATION, LOCATION, LOCATION

Location awareness is no longer a feature reserved for mobile devices and their applications. Mobile IT platforms and their deployments are no longer rarities, or niche solutions preferred by selected industries such as transportation and defense. These trends have been accelerated to address distributed IT and the Internet of Things across industries.



Timekeeping does not have to be a stand-alone capability. A combination of persistent UTC-accuracy and awareness of true geolocation would enable a wide range of new platform capabilities for modern IT solutions. Sync-n-Scale hardware interfaces are capable of exploiting the full complement of GPS signals for positioning and timing, and delivering them to the OS and its hypervisor for general-purpose computing applications. Current Geolocation API **[40]** support in Windows OS is uneven between the in-market server and desktop editions. Unlike the VM IC Time Synchronization support, there is not yet a similar mechanism for Hyper-V VMs or comparable platform abstractions such as Containers to obtain their true geographical location.

When properly aggregated, the yielded functionalities are greater than the sum of these discrete capabilities. Cloudbased applications can be demonstrated to operate in compliance with data sovereignty regulatory requirements. Cost effective software defined mitigation for GPS-denied conditions can be devised to operate without exotic hardware.

VII.1. DATA SOVEREIGNTY COMPLIANCE

A data center is where application software and customer data running on the software are located. Vendors of cloudbased IT services must maintain transparency of where they replicate customer data at any given time for protection against failure or local disaster. If a data center ceases functioning for any reason, customer data will not be lost if the application software and customer data running on that application software are also available from a second or possibly third data center. And assuming it works smoothly enough, customers might not even be notified when such a failover occurs. Depending on the particular service, failover may not result in any service interruption at all.

Global enterprises have leveraged the internet and cloud-based computing services, along with data centers, to establish private communication networks and capture efficiencies from global technology. As a result of such globalization, in recent years, many nations have issued geographic location rules restricting how corporations can handle and transmit their customers' data across borders, including through these private networks. Certain entities require that specific types of data, for example government data, employee data, or telecommunications traffic data be stored within a limited geographical border, and in some cases, such data may even not be accessed from outside of a geographical border.

True GPS-originated information can be used to determine if specific applications, data stores, virtual machines or comparable platform abstractions are definitively inside particular geofences before being allowed to operate. Continuous records of both positioning and timing during their operations would allow developers and IT professionals to gain insights into the reliability, availability, serviceability and locality of their cloud-based IT components.

VII.2. SOFTWARE DEFINED MITIGATION FOR GPS-DENIED CONDITIONS

GPS positioning and timing data services are critical to industry sectors such as energy (*e.g.* power grids), financial services (*e.g.* banking and market transaction systems) and information technology (*e.g.* data centers). As these critical infrastructure assets, networks, and systems have become much more interdependent across vast regions, crossing jurisdictional and national boundaries, and time zones, the need for operational awareness of accurate and precise timing services is vital to their continued functioning.

Worldwide outage of GPS infrastructure caused by man-made calamity and conflicts is outside the scope of this paper. Local and regional GPS disruption is more common. This section proposes a possible civil defense mitigation approach to be built on UTC-accuracy and geolocation awareness of IT resources in the private sectors.

In order to detect GPS-denied conditions or timing manipulation, we must be able to ascertain what is nominally UTC-accurate time at all time, and from a trusted reference time source. We are proposing an ensemble of UTC-accurate and geolocation-aware (*e.g.* Sync-n-Scale enabled) systems operated in tandem with participating NISTDC units, at NIST and privately operated elsewhere in the world (Fig. 28). These NISTDC units might be located at commercial data centers, or public premises such as college campuses, or local government facilities.

Collectively these systems form mesoscale networks of "sensors" for detecting GPS disruption and timing manipulation. As a discreet and fungible network of sensors, they would transmit their own operating positioning and timing information, and the full complement of GPS ephemeris data as received by each. These data points are then aggregated in cloud space for machine learning and deep learning analytics.

Aggregated sensor data and their analytical logics are protected and access controlled in cloud space. Reported GPS ephemeris data or their absence from these sensors can be analyzed in real time to identify the specific sensors being affected by GPS disruption or subjected to timing manipulation, abruptly or gradually over time. Size of the affected locale or region can be determined from the aggregated geolocations of affected sensors.



VIII. SUMMARY

Timekeeping for modern IT solutions can be more effective by bringing an authoritatively curated time source directly into general-purpose computing platforms to make them persistently UTC-accurate; refining tools, methods and harnesses to help IT professionals demonstrate that their IT solutions are UTC-accurate; and exploiting these new capabilities fully in future solutions.

IX. ABOUT THE AUTHORS



Son VoBa joined Sync-n-Scale (and its parent company NEL Frequency Controls, Inc.) in 2014 as the Director of Cloud & Enterprise Solutions following a 15-year stint in technical program management positions at Microsoft Corporation in Windows Server and related organizations. Prior to joining Microsoft in 2000, Mr. VoBa had spent 20 years at Digital Equipment Corporation (DEC), which later was acquired by Compaq Corporation. He graduated from Purdue University in West Lafayette, Indiana with degrees in Science and Computer Science. His work at Sync-n-Scale and Microsoft resulted in granted and pending patents in the areas of basic computing, applied RFID in data center infrastructure, virtual appliance distribution, and applied cryptography for trustworthy computing.



Charles Ulland is the Managing Director of Sync-n-Scale. In addition to this role, Mr. Ulland also serves as a Managing Director at NEL Frequency Controls, Inc., a company he has been affiliated with since 1990. He did his undergraduate study at Minnesota State University at Mankato, and completed his Master of Business Administration (MBA) at University of Wisconsin in Milwaukee.



Michael Lombardi is a metrologist in the Time and Frequency Division of the National Institute of Standards and Technology (NIST) in Boulder, Colorado. He is the project manager for the NIST Frequency Measurement and Analysis (FMAS) and Time Measurement and Analysis (TMAS) remote calibration services and the Time and Frequency Division Quality Manager. Mr. Lombardi served as the chair of the Time and Frequency Metrology Working Group of the Interamerican Metrology System (SIM) from 2011 to 2015 and as the program chair of PTTI in 2017. He has published over 100 papers related to time and frequency metrology.



Arno Lentfer embarked on his one-man multi-year Windows Timestamp Project a decade ago. He is the owner and curator of its independent repository of deep-dive knowledge about how Windows OS keeps time, and the G Suite tools which had been around a great deal longer than the recently renamed Google Apps (to also be G Suite). Mr. Lentfer received patents in the fields of X-ray lithography and goniometric systems. He co-founded X-ray Research GmbH in 1990 and spent the next 15 years developing these technologies before retiring to become a freelance hardware and software designer.

X. ACKNOWLEDGMENTS

This paper includes contributions from the U. S. government, and as such, is not subject to copyright. The use and mention of commercial products does not imply endorsement by NIST.

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